



MANIPAL UNIVERSITY JAIPUR

School of Electrical, Electronics and
Communication Engineering
Department of Electrical Engineering

Lab & Equipment Details (IE Lab)

Location: 1AB Building, Room No. 203

Name of Laboratory	Integrated Electronics Laboratory
Lab Description	The lab is equipped with the latest hardware and software tools to provide hands-on practical experience. Experiments are designed to perform on breadboard (Advanced Digital and Analog Kit) with the help of different types of electrical & electronics components, DSO, function generator and DC power supply. Advanced experiments are designed to perform on FPGA interfaced with computer having supported Xilinx Webpack software (VHDL/Verilog language).
Lab Resources	<ol style="list-style-type: none"> 1) ADVANCED FPGA TRAINER KIT (XC3S400, Spartan-3) 2) Genesys ZU- Zynq UltraScale+ MPSoC for Embedded Development Board. 3) PYNQ-Z1 Python Productivity for Zynq-7000 ARM/FPGA SoC 4) Digital Lab Trainer Board & Power Project Trainer Board facilitated with bread boards. 5) DC Power Supply (0-30) V 6) Function Generator (10 Mhz) 7) DIGITAL STORAGE OSCILLOSCOPE 100 Mhz 8) 3½ DIGIT DIGITAL MULTIMETER 9) Consumable Electrical Components 10) Consumable Electronics Components (ICs)