

MANIPAL UNIVERSITY JAIPUR
Faculty of Engineering
B.Tech in Electronics Engineering (VLSI Design and Technology) -160 credits
2023 onwards

Year	FIRST SEMESTER						SECOND SEMESTER					
	Sub. Code	Subject Name	L	T	P	C	Sub. Code	Subject Name	L	T	P	C
I	CY10XX	Engineering Chemistry + Lab	2	1	0	3	PY1001	Engineering Physics + Lab	3	1	0	4
	MA10XX	Mathematics 1	3	0	0	3	MA20XX	Mathematics 2	3	0	0	3
	EC10XX	Basic Electrical Engineering	3	0	0	3	XXXXXX	Environmental Studies	2	0	0	2
	CV10XX	Basic Structural Engineering	3	0	0	3	ME10XX	Basic Mechanical Engineering	3	0	0	3
	CE10XX	Biology for Engineers	2	0	0	2	ECE1001	Basic Electronics	3	0	0	3
	CS10XX	Computer Programming+ Lab	3	1	0	4	XXXXXX	Creativity & Innovation Lab	0	0	4	2
	XX10XX	Arduino, IoT Fab Lab	0	0	2	1	XXXXXX	Engineering Graphics Lab	0	0	2	1
	XXXXXX	Constitution of India	1	0	0	1	XXXXXX	Technical Writing Clinic 1	1	0	0	1
							DA10XX	Universal Human Values	1	0	0	1
	Total Credits	17	2	2	20		Total Credits	16	1	6	20	
	Total Contact Hours (L + T + P)	21				Total Contact Hours (L + T + P) + OE	23					

Scheme for higher semesters coming soon...